

# Opella-XD for ARC

High-speed JTAG/cJTAG Debug Probe



## Overview

Ashling's **Opella-XD** is a powerful cJTAG/JTAG debug probe for embedded development with Synopsys' DesignWare ARC™ configurable RISC processors.

Developed in cooperation with Synopsys, the Opella-XD probe integrates with the MetaWare Development Toolkit or GNU GDB Debuggers under Windows™ and Linux based hosts.

Opella-XD provides up to 3MB/s download speeds making it suitable for large, complex, software-intensive projects.



## Opella-XD Features

- Fast, trouble-free "plug-and-play" installation using USB 2.0 High-Speed Interface (480Mb/s)
- Powered by USB interface; no external power-supply required
- Fine-grained adjustment of cJTAG/JTAG clock frequency from 1kHz to 100MHz
- Auto-conditioning support provides maximum possible download speed to target with fastest cJTAG/JTAG clock frequencies
- Hot-plug support; allows connection to a running target without resetting or halting
- Supplied with 20-way 0.1" cJTAG/JTAG cable for debug interface to target device or target FPGA
- Supports target operating voltages from 0.9V to 3.6V. Opella-XD detects and automatically configures for the appropriate target voltage
- Configurable Target-Reset and Test-Port-Reset, under full user control
- Built-in diagnostics instantly show status of Target, Debug Probe and USB link
- Display/read/write of target system memory and peripheral registers
- Support for all on-chip hardware breakpoints; unlimited software breakpoints
- Run/stop control of target application including go, halt, step over, step into and step out of
- Operates with MetaWare Development Toolkit or GNU GDB Debuggers under Windows™ or Linux based hosts
- Multi-core debug support – up to 256 cores on single or multiple cJTAG/JTAG interfaces
- Supports all ARC cores including HS, EM, ARC 700, ARC 600 and AS2xx

## Order Codes

Product	Order Code
Opella-XD for ARC Debug Probe. Supplied with 20-way cJTAG/JTAG cable.	Opella-XD-ARC
Opella-XD for ARC Software. Software drivers for MetaWare and GNU GDB Debuggers.	ARC-Software
15-way JTAG adapter for connection to the ARCangel prototyping system.	AD-ARC-D15
6-way cJTAG/JTAG adapter for connecting Opella-XD to target hardware with a 6-pin connector.	AD-ARC-6JTAG

DS323-Opella-XD-ARC v1.5 Ashling Microsystems Ltd, National Technology Park, Limerick, Ireland. Tel: +353-61-334466 Email: [sales@ashling.com](mailto:sales@ashling.com)

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